Substitute for form 1449A/PTO INFORMATION DISCLOSURE					Complete if Known		
					Application Number	10/676,883	
					Filing Date	9/38/03	
STATEMENT BY APPLICANT				ANT	First Named Inventor	Christopher L. Rumer	
					Art Unit	2891	
			$\overline{}$		Examiner Name	C. Everbant	
Sheet	11	of	f	1	Attorney Docket Number	42P16840	

NON PATENT LITERATURE DOCUMENTS								
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T,					
CME		RECHE, J., "Wafer Thinning and Thru-Silicon Vias", The Path to Wafer Level Packaging, 41 pages, Tru-Si Technologies, www.trusi.com, IEEE/CPMT Meeting, Santa Clara (CA)						
		05/10/2000.						
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Signature	C. /werhart	Considered	1-20-0-
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<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>&#</sup>x27;Applicant's unique citation designation number. \*Applicant is to place a check mark here if English language Translation is attached.